

### **What is Claimed is:**

1. An ESD protection configuration for light emitting diodes (LED), comprising:

a resistor, having a first end connected to an n-node of an LED; and

a capacitor, having a first end connected to a second end of the resistor and a second end connected to a p-node of the LED, wherein the LED, resistor and capacitor are mounted onto a circuit substrate ;

whereby the RC circuit engaging a parallel connection with the LED is able to guard the LED against spikes, absorb and remove ESD and prevents the LED from burning to effectively extend the lifespan of the LED.

2. The ESD protection configuration for LED of Claim 1, wherein the resistor and the capacitor are disposed outside the circuit substrate and connected to the LED via the circuit substrate.
3. The ESD protection configuration for LED of Claim 1, wherein the resistor and an equivalent capacitor are disposed inside the circuit substrate and connected to an p-n junction of the LED via the circuit substrate.
4. The ESD protection configuration for LED of Claim 3, wherein the equivalent capacitor includes a first capacitor and a second capacitor that are connected in series and has an equivalent capacitance represented by the formula,  $1/C=1/C_1+1/C_2$ .
5. The ESD protection configuration for LED of Claim 3, wherein the p-n junction of the LED and the p-n junction of the circuit substrate are provided with a metal conductive layer and an insulator.
6. The ESD protection configuration for LED of Claim 1, wherein the capacitor is made of a material having a high dielectric constant greater than 3.9.
7. The ESD protection configuration for LED of Claim 5, wherein the

material having a high dielectric constant is selected from one of the group consisting of:  $\text{SiN}_x$ ,  $\text{SiO}_2$ ,  $\text{TiO}_2$ ,  $\text{TiN}$  and  $\text{BaTiO}_3$ , or their series material with dielectric constant be higher than 3.9.

8. The ESD protection configuration for LED of Claim 1, wherein the resistor has an ideal resistance of  $1\text{K}\Omega$ .
9. The ESD protection configuration for LED of Claim 1, wherein the LED is a high speed semiconductor, such as Groups III-V nitride semiconductors containing  $\text{InGaN}$ .
10. An ESD protection configuration for LED, comprising:
  - an LED, having a p-n junction and connected to a circuit substrate, the circuit substrate having two p-type substrates and one n-type substrate therein;
  - a first ESD protection configuration, built-in the circuit substrate and including a first resistor, a first capacitor and a first diode that are connected in series and then engage a parallel connection with the LED, wherein the first diode has a p-node connected to an n-node of the LED; and
  - a second ESD protection configuration, built-in the circuit substrate and including a second resistor, a second capacitor and a second diode that are connected in series and then engage a parallel connection with the LED and the first ESD protection configuration, wherein the second diode has a p-node connected to the p-node of the LED.
11. The ESD protection configuration for LED of Claim 10, wherein the first capacitor has a first capacitance greater than a second capacitance of the second capacitor.
12. The ESD protection configuration for LED of Claim 11, wherein the first capacitance ranges between  $100\text{pF}$  and  $100\text{nF}$ .
13. The ESD protection configuration for LED of Claim 11, wherein the second capacitance ranges between  $100\text{pF}$  and  $100\text{nF}$ .

14. The ESD protection configuration for LED of Claim 10, wherein the first capacitor includes a third capacitor and a fourth capacitor that are connected in series and has an equivalent capacitance represented by the formula,

$$1/(C_{\text{first capacitor}}) = 1/(C_{\text{third capacitor}}) + 1/(C_{\text{fourth capacitor}})$$

15. The ESD protection configuration for LED of Claim 10, wherein the second capacitor includes a fifth capacitor and a sixth capacitor that are connected in series and has an equivalent capacitance represented by the formula, :

$$1/(C_{\text{second capacitor}}) = 1/(C_{\text{fifth capacitor}}) + 1/(C_{\text{sixth capacitor}})$$

16. The ESD protection configuration for LED of Claim 10, wherein the capacitor is made of a material having a high dielectric constant greater than 3.9.
17. The ESD protection configuration for LED of Claim 16, wherein the material having a high dielectric constant is selected from one of the group consisting of:  $\text{SiN}_x$ ,  $\text{SiO}_2$ ,  $\text{TiO}_2$ ,  $\text{TiN}$  and  $\text{BaTiO}_3$ .
18. The ESD protection configuration for LED of Claim 10, wherein the first resistor and the second resistor are made of a material having a low dielectric constant.
19. The ESD protection configuration for LED of Claim 10, wherein the first resistor and the second resistor each have an ideal resistance of  $1\text{K}\Omega$ .
20. The ESD protection configuration for LED of Claim 10, wherein the first diode and the second diode are constructed the two p-type substrates and the n-type substrate in the circuit substrate.
21. The ESD protection configuration for LED of Claim 10, wherein the LED is a high speed semiconductor, such as Groups III-V nitride semiconductors containing  $\text{InGaN}$ .
22. An ESD protection method for LED, comprising the steps of:

- a. providing a resistor, the resistor having a first end connected to

an n-node of an LED; and

b. providing a capacitor, the capacitor having a first end connected to a second end of the resistor and a second end connected to a p-node of the LED;

whereby the RC circuit engaging a parallel connection with the LED is able to guard the LED against spikes, absorb and remove ESD and prevents the LED from burning to effectively extend the lifespan of the LED.

23. An ESD protection method for LED, comprising the steps of:

a. providing an LED, the LED having a p-n junction and connected to a circuit substrate, the circuit substrate having two p-type substrates and one n-type substrate therein;

b. providing a first ESD protection configuration, built-in the circuit substrate and including a first resistor, a first capacitor and a first diode that are connected in series and then engage a parallel connection with the LED, wherein the first diode has a p-node connected to an n-node of the LED; and

c. providing a second ESD protection configuration, built-in the circuit substrate and including a second resistor, a second capacitor and a second diode that are connected in series and then engage a parallel connection with the LED and the first ESD protection configuration, wherein the second diode has a p-node connected to the p-node of the LED